

COMPATIBILITY

VSF TR-10

AMWA NMOS

SMPTE ST 2110

SMPTE ST 2059

SMPTE ST 2022-7

APPLICATIONS

Broadcast & Media

Corporate

Education

Healthcare

Public Sector

Retail & Hospitality

SPECIFICATIONS

TX & RX

HD – 4Kp30 resolutions

RGB, YCbCr 4:4:4 & 4:2:2

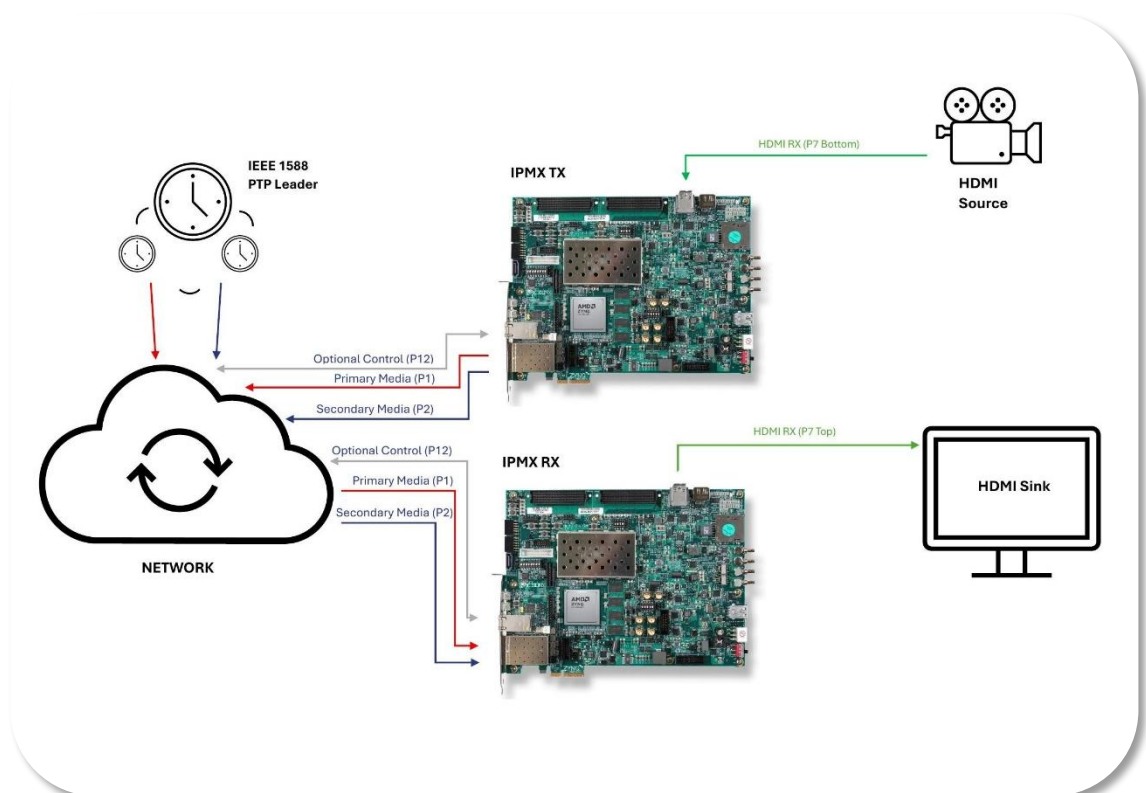
8, 10, 12-bit* colour depths

10G network

Reference Design for IPMX Products

Reference Design

The ITDev IPMX reference design provides a pre-sales evaluation environment for the AMD Zynq™ UltraScale+™ ZCU106 MPSoC platform, demonstrating HDMI I/O and IPMX network operation up to 10G. It helps equipment manufacturers assess the IP core, reference software and integration approach before product implementation.



- Uncompressed & Compressed Video**
- Synchronous & Asynchronous Video
- Precision Time Protocol
- NMOS Discovery, Management & Control
- HDCP Protection**
- Seamless Protection Switching
- Uncompressed & Compressed Audio
- Offers sub-frame latency

*RX only **Coming Soon

Reference Design

Video	HDMI In, HDMI Out	High-speed	Evaluation-dependent expansion interfaces
Network	10G Ethernet	Debug	JTAG, USB console
USB	USB-UART / console	Storage	Not required for standard evaluation
Power	12V input		

AMD
together we advance_

Standards Compatibility

VSF TR-10-1	System Timing / Precision Time Protocol (PTP)
VSF TR-10-2	Uncompressed Active Video
VSF TR-10-3	PCM Digital Audio
VSF TR-10-5	HDCP Key Exchange Protocol**
VSF TR-10-6	Forward Error Correction (FEC)**
VSF TR-10-8	NMOS Requirements
VSF TR-10-9	System Environments and Device Behaviour
VSF TR-10-11	Constant Bit-Rate Compressed Video**
VSF TR-10-12	AES3 Transparent Transport
VSF TR-10-13	Privacy Encryption Protocol (PEP)**
ST 2022-7	Seamless Protection Switching
ST 2110	-10, -20, -21, -22**, -30, -31
ST 2059	-1, -2 PTP Timing Environments

*RX only **Coming Soon

Deliverables

Reference Design	Pre-sales evaluation package, including documentation
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